Appl. No. 10/721,310 Amdt. Dated November 20, 2006 Reply to Office Action of July 20, 2006

Attorney Docket No. 81788.0261 Customer No.: 26021

NOV 2 2 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Go Iwasaki

Serial No: 10/721,310

Confirmation No.: 7084

Filed:

November 25, 2003

For:

OUTPUT BUFFER CIRCUIT AND

SEMICONDUCTOR MEMORY

USING THE SAME

AMENDMENT AND RESPONSE

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This is in response to the Office Action dated July 20, 2006. The time for responding to the outstanding Office Action is extended one month by the accompanying petition from October 20, 2006 to November 20, 2006. Please amend the above-referenced application as follows:

The claims are listed starting on page 2 of this paper.

Remarks begin on page 6 of this paper.

Art Unit: 2827

Examiner: Thong Quoc Le

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop Amendment Commissioner for Patents P.O. Box 1450
Alexandria, VA 22313-1450
November 20, 2006
Date of Deposit
William H. Wright, Reg. No. 36,312
Name
Signature
Date